

FIG. 1

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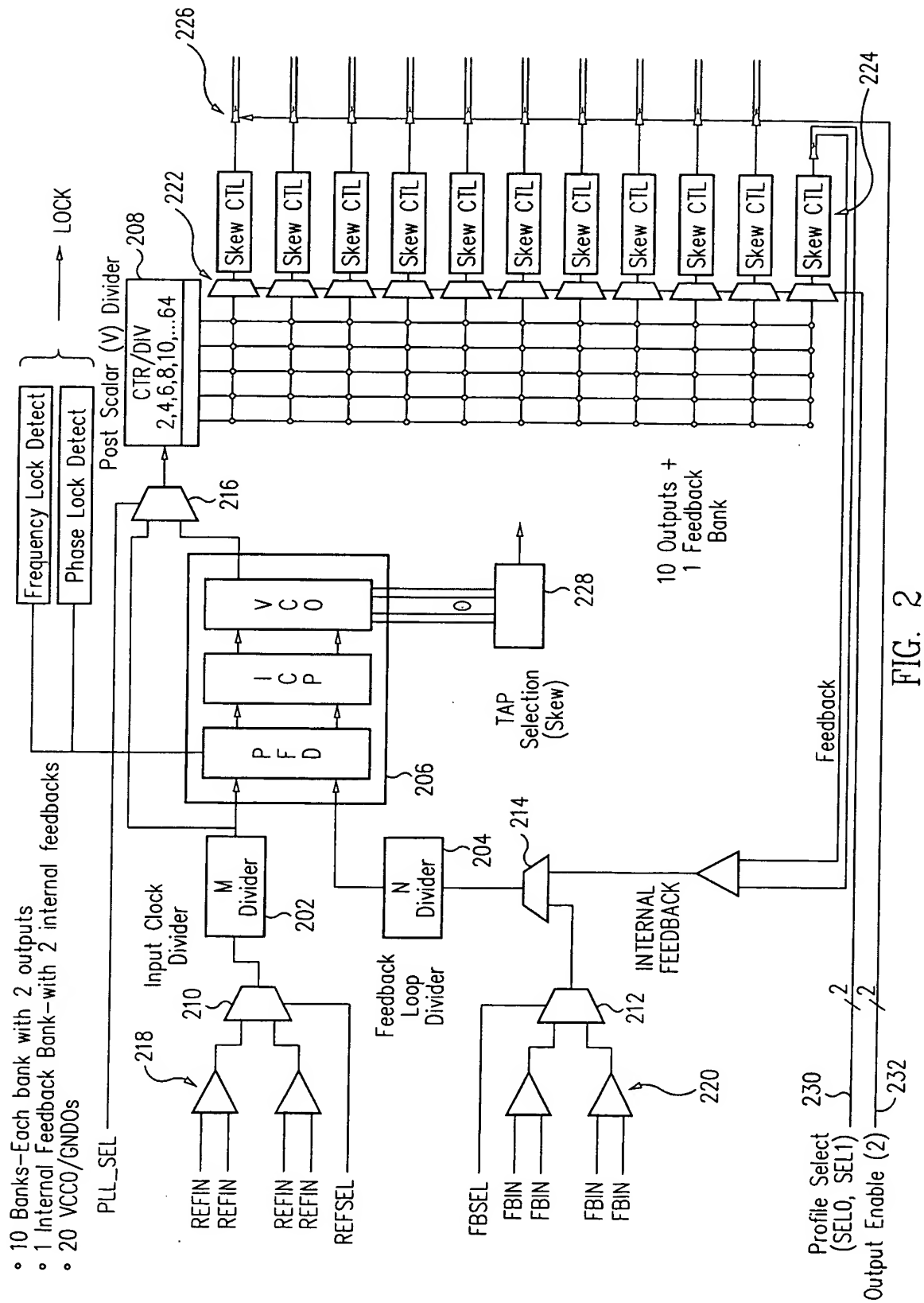


FIG. 2

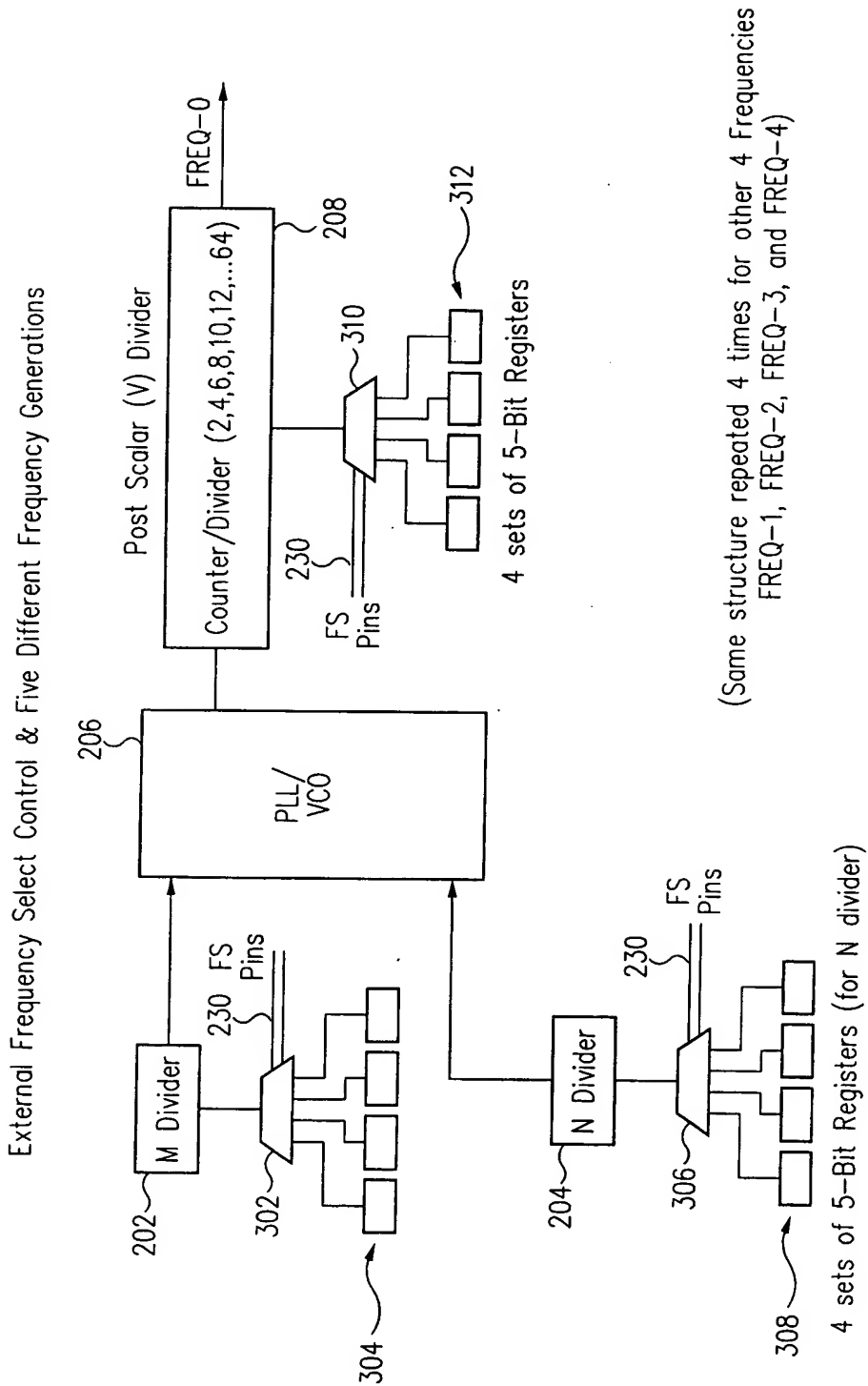


FIG. 3

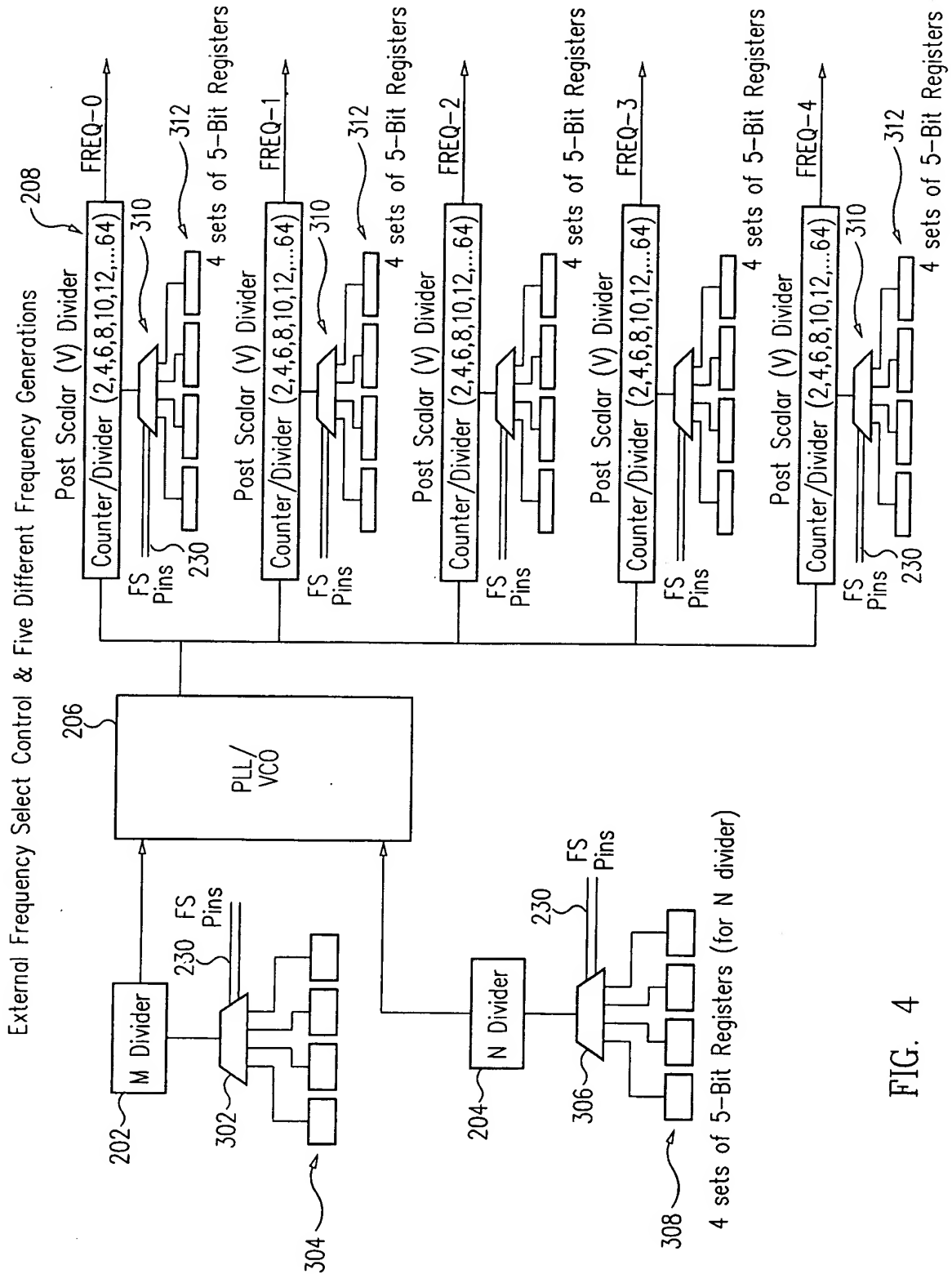


FIG. 4

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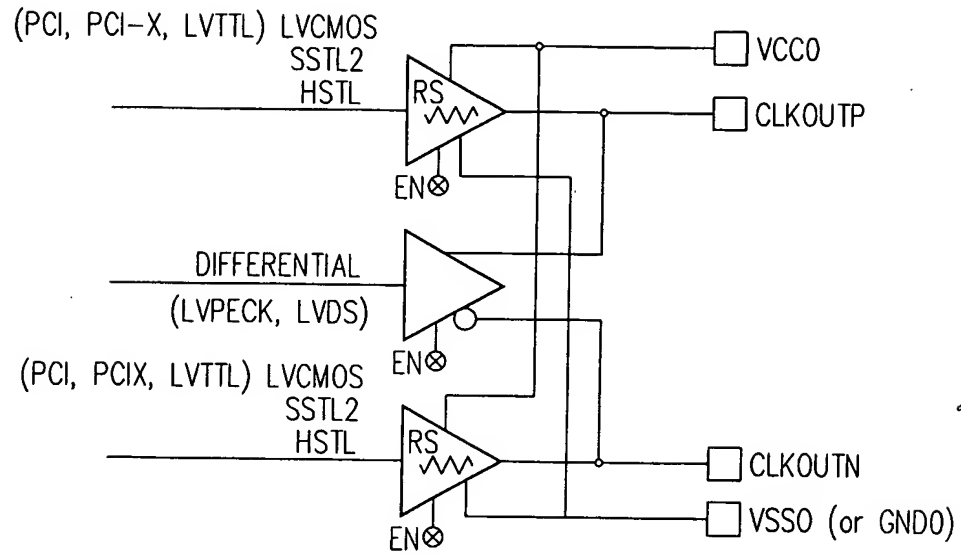


FIG. 5

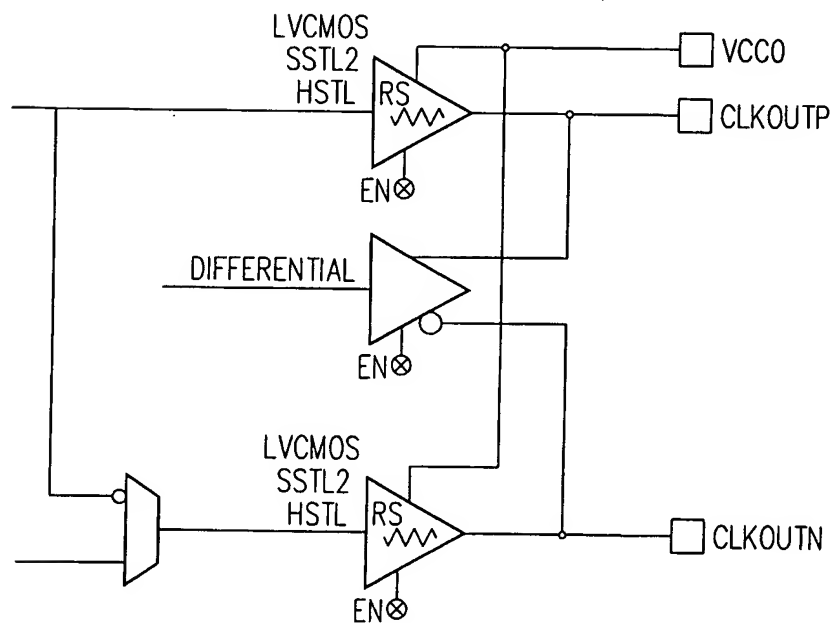


FIG. 6

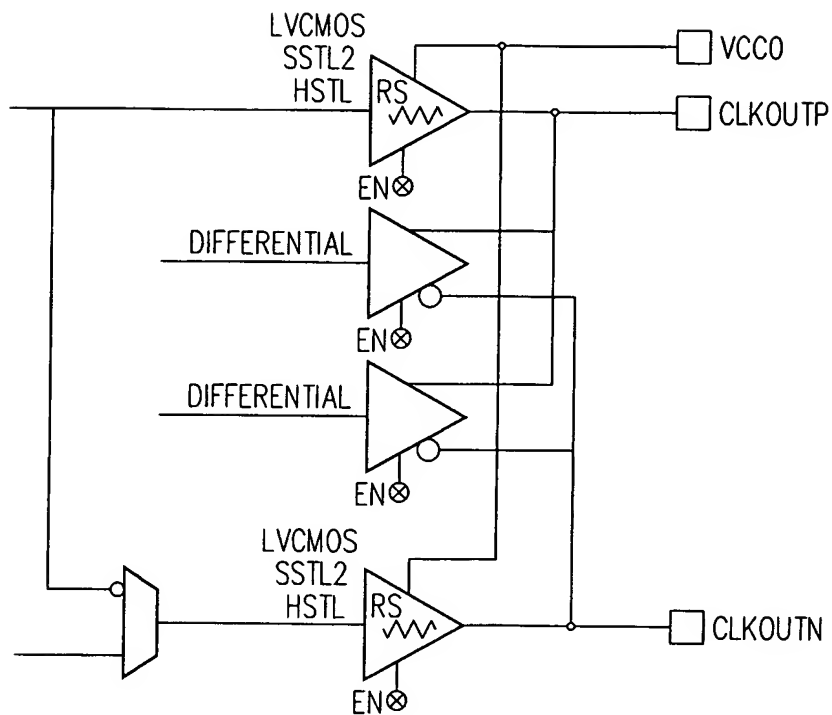


FIG. 7

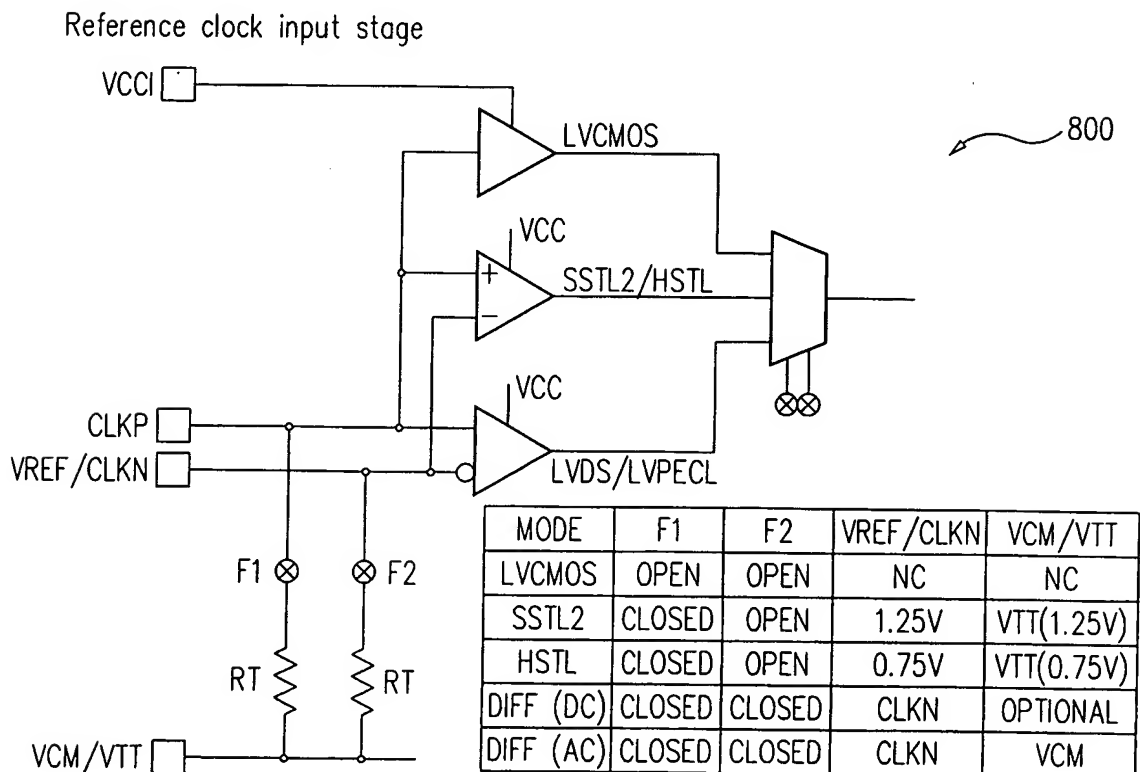


FIG. 8

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SSTL2/HSTL input application

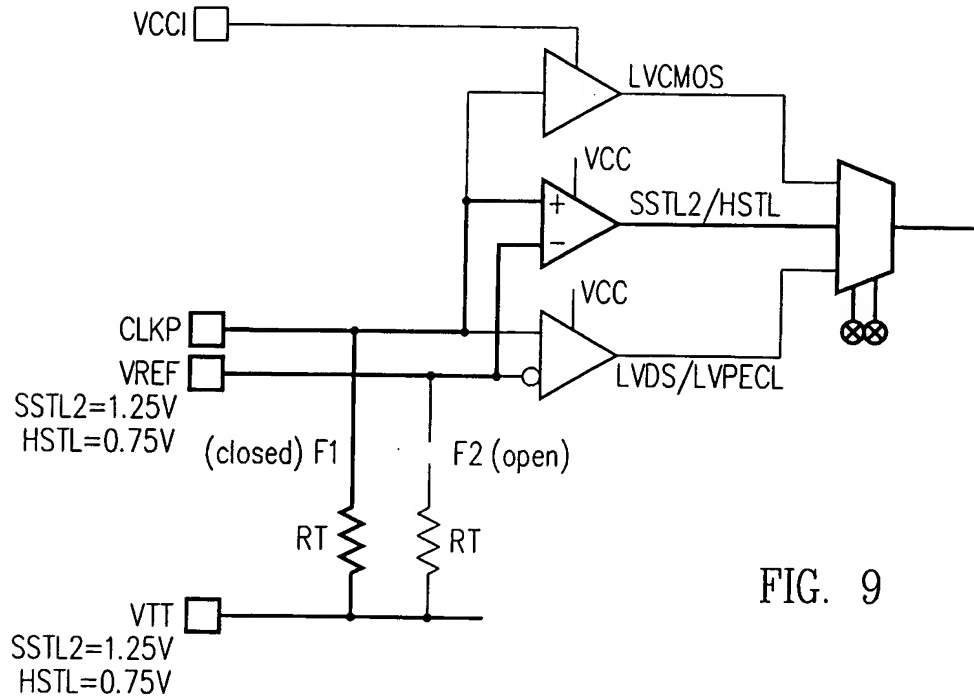


FIG. 9

Alternative DC coupled differential application

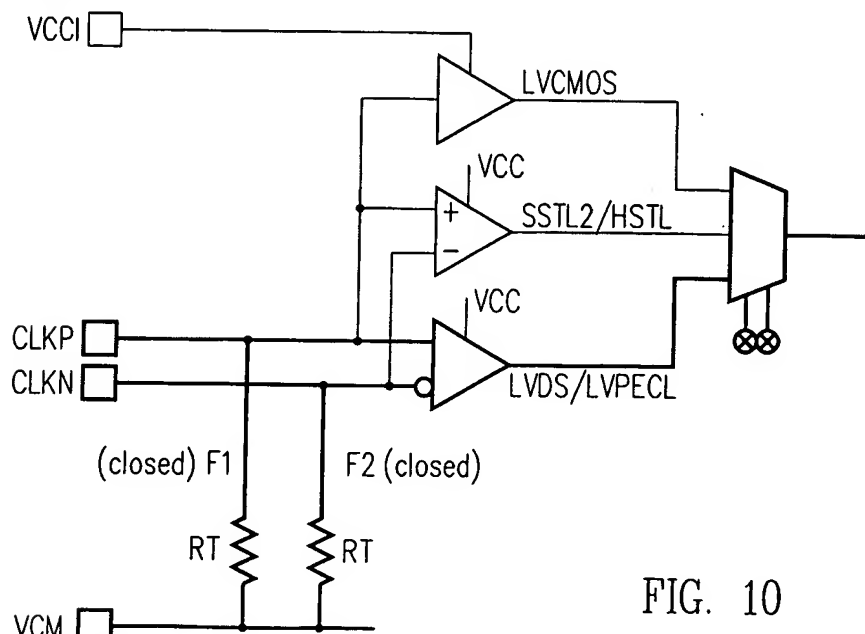
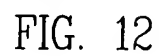
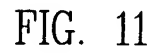


FIG. 10

(drive to optimum VCM
or decouple with cap to GND)



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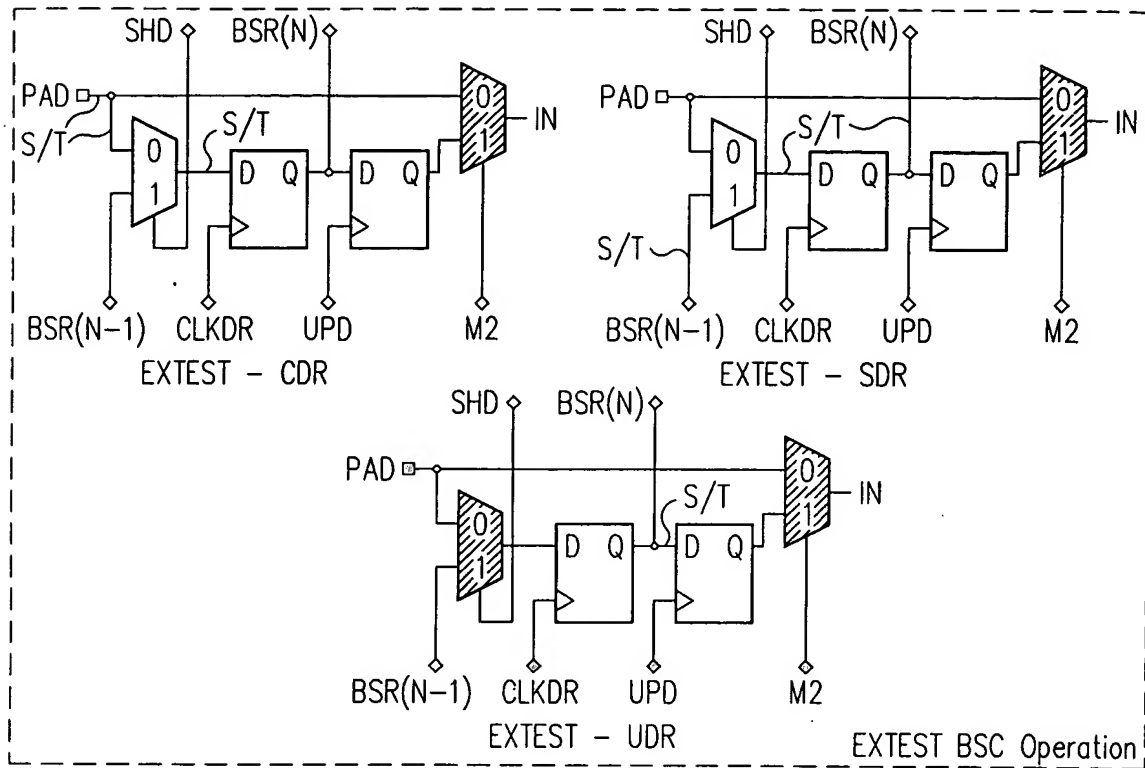


FIG. 13

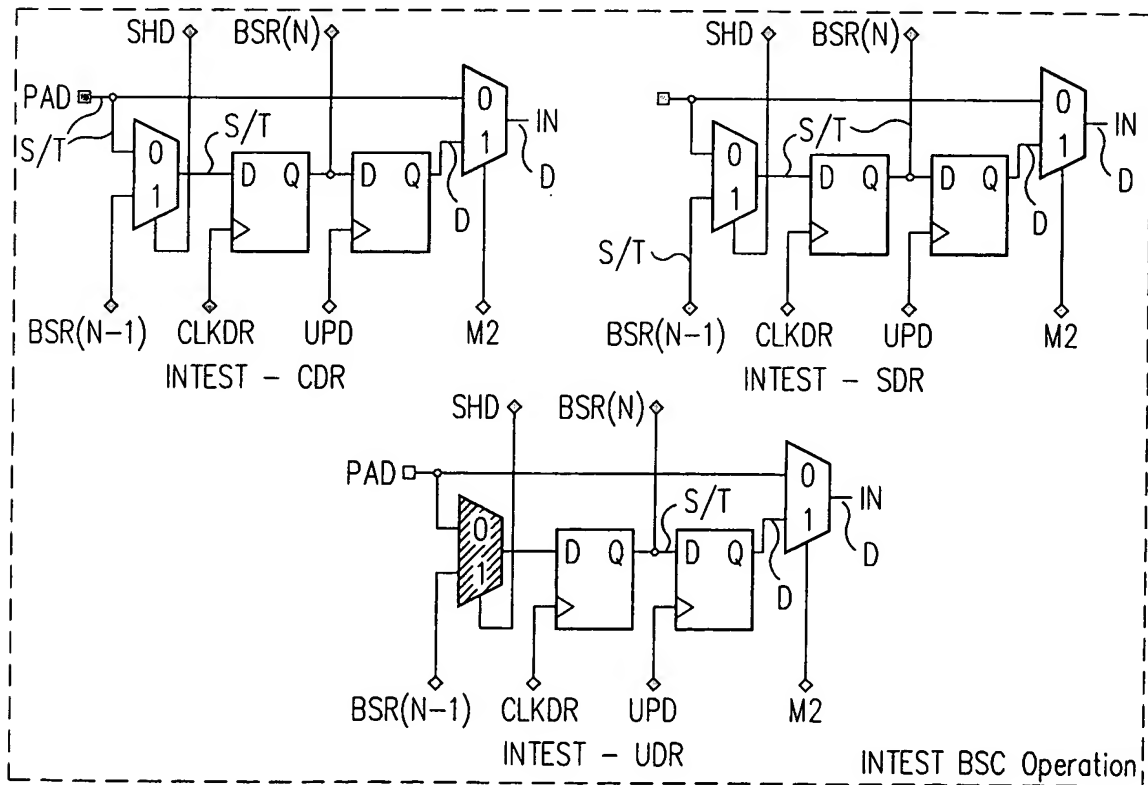


FIG. 14

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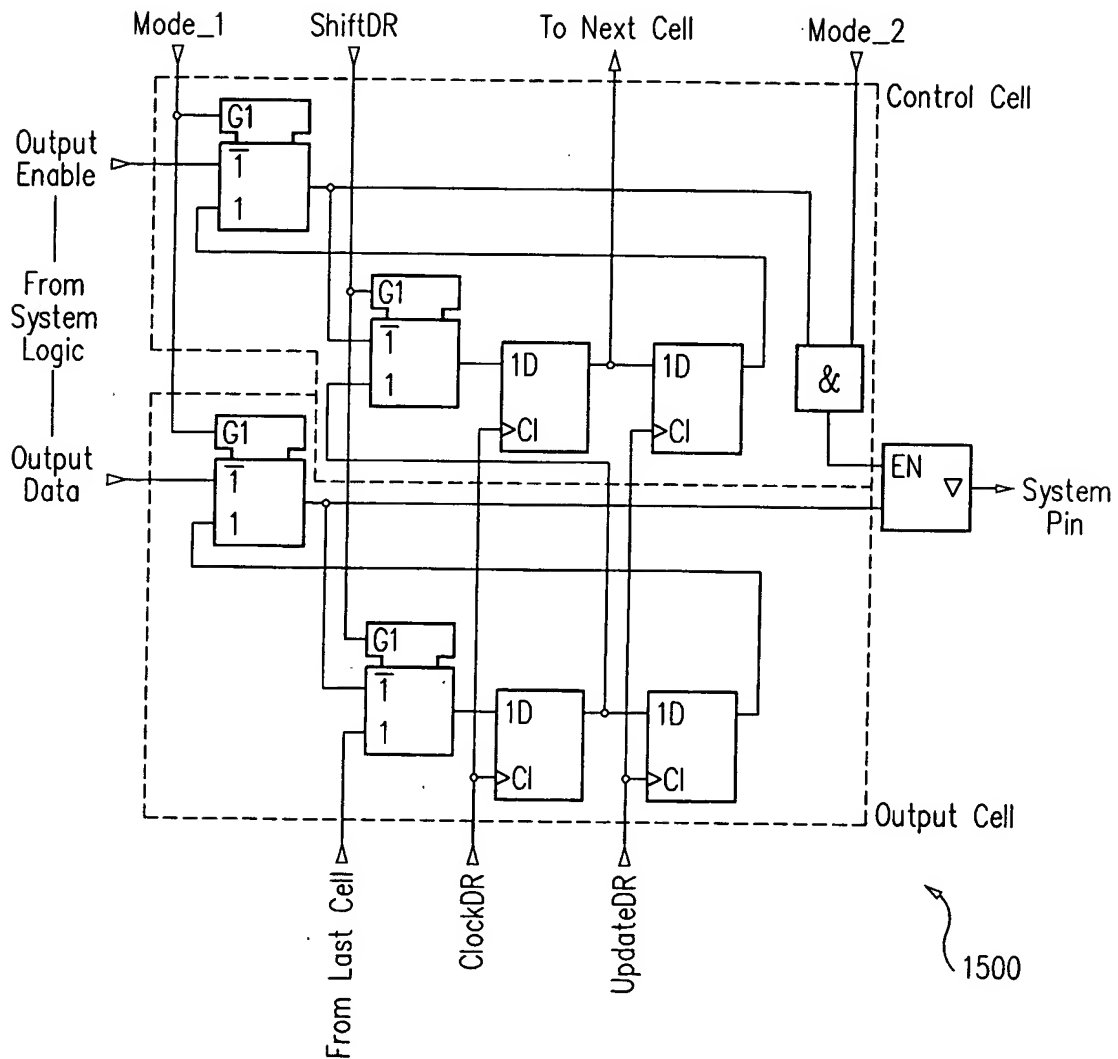


FIG. 15